



University of Pune



Common Entrance Test Registration Form

Affix your passport size photograph

Certificate Course in Embedded System Design
(Course duration – 6 months, full time)

Name :
(Write your name as you want it to be printed on the Certificate)

Address :

Pin Code

Telephone No. : e-mail:

Date of Birth : Sex: M F

Are you interested in pursuing MS programme of ISEP at Paris? Yes No

Father's Name: Mother's Name:

Occupation: Telephone No.:

Educational Qualification: (Attach Photocopy of Mark Sheet / Certificate of Highest Qualification)

Examination	Year of passing	% of marks	Name of College	University / Board
Master's Degree (Engg./ Science)				
Bachelor Degree (Engineering)				
3 rd year (Engg.) / Bachelor (Science)				
2 nd year (Engg. / Science)				
1 st year (Engg. / Science)				
Diploma (Engg.)				
H.S.C. (12 th Standard)				
S.S.C. (10 th Standard)				

Category of applicant: Open SC ST DTNT OBC

Reserved category students please enclose attested photocopy of Caste Certificate and Validity Certificate.

I hereby declare that the information given above is true to the best of my knowledge. I have read the selection procedure set forth in the prospectus and related official announcements. I declare that I accept the terms and conditions set forth herein. I recognize the financial obligations & payment dates as stated.

Date: Applicant's Signature

Entrance Exam Fee: Rs. 500/- only (Pay by Cash or DD in favour of "ICIT Pvt. Ltd., Pune")

Demand Draft No.: Date: Bank:

Preferred CET Centre	Preferred CET Date	Preferred CET Time
<input type="text"/>	<input type="text"/>	<input type="text"/>

Application Form

**ONLY FOR THE INFORMATION OF STUDENT
(DO NOT SEND WITH THE APPLICATION FORM)**

Fee schedule	Certificate Course in Embedded System Design
Fee Installment	Amount (Rs.)
Confirmation of Admission	Rs. 50,000/-
1 st installment (within 30 days of course commencement)	Rs. 35,000/-
Total course fee	Rs. 85,000/-

CET Details and Admission Procedure

Issue of Hall Tickets: Hall tickets will be sent by e-mail after receiving completed application forms and DD (which should then be printed by the student). It is compulsory for student to carry the hall ticket to the testing center. Address of the testing center will be printed on the hall ticket.

The preferred date and time mentioned by the candidate for a testing centre will be considered depending upon the availability and if the application forms are received on time, otherwise the next available date and time at that centre will be provided by ICIT which has to be accepted by the candidate. This will be notified to the candidate via email.

CET fee is Rs. 500.00 and may be paid:

- a) By Cash in ICIT office.
OR
- b) By Demand Draft – payable to “ICIT Pvt. Ltd., Pune”.
Kindly send by speed post/courier along with the filled Application form.
OR
- c) In ICIT bank account: (send a scanned image of the payment slip to icitonline@gmail.com)
Bank Name: Syndicate Bank, Shivajinagar Branch, Pune
Account Name: ICIT Pvt. Ltd.
Account Number: 53211010006135
IFSC Code: SYNB0005321
PAN: AAAC17348N

Please submit copies of your semester wise mark lists OR copy of the final Degree Certificate.

Address for correspondence:
ICIT Pvt. Ltd.,
Department of Electronic Science,
Savitribai Phule Pune University,
Ganeshkhind road,
Pune 411 007.

❖ **Certificate Course in Embedded System Design**

- ✓ **CET dates : September 1st and 2nd, 2018**
- ✓ **Batch Start date : September 22th, 2018**

❖ **Last date of application : August 30th 2018**

(Online) Availability of Testing			
Centre Name	Mode of Exam.	September 1st	September 2nd
Ahmedabad	Online	Available	Available
Chennai_CD	Online	Available	Not Available
Delhi	Online	Available	Available
Secunderabad	Online	Available	Available
Indore	Online	Available	Not Available
Kolhapur	Online	Available	Not Available
Kolkata_NA	Online	Available	Available
Lucknow_RO	Online	Available	Not Available
Mumbai_DDR1	Online	Available	Available
Nagpur	Online	Available	Not Available
Nasik	Online	Available	Not Available
Bengaluru	Online	Available	Available
Test Time slots are 10:00 AM to 12:00 PM, 12:30 PM to 14:30 PM , 3:00 PM to 5:00 PM			